

In re Patent Application of

Kenichi KOYANAGI, et. al.

Serial No.: 10/630,444

Filed: July 29, 2003

For: METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE

Confirmation No.: 7918

Date: October 20, 2006

Group Art Unit: 2823

Examiner: William D. COLEMAN

VIA EFS-WEB

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

AMENDMENT/SUBMISSION

Sir:

This is a response to the Final Office Action mailed August 3, 2006 in the above-identified application. Reconsideration of the application is respectfully requested.

FEE CALCULATION

Any additional fee required has been calculated as follows:

_____ If checked, "Small Entity" status is claimed.

	NO. CLAIMS AFTER AMENDMENT		HIGHEST NO. PREVIOUSLY PAID FOR		EXTRA PRESENT		RATE	ADDIT. FEE
TOTAL	37	MINUS	37	* =	0	X	(\$25 SE or \$50)	\$0
INDEP.	3	MINUS	3	** =	0	X	(\$100 SE or \$200)	\$0
FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM						X	(\$180 SE or \$360)	\$0

* not less than 20

** not less than 3

TOTAL \$ 0

If any additional payment is required, credit card payment in the amount of \$ _____ is submitted via EFS-WEB.